

Closing slides

EnA-HPC 2013

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4th EnA-HPC

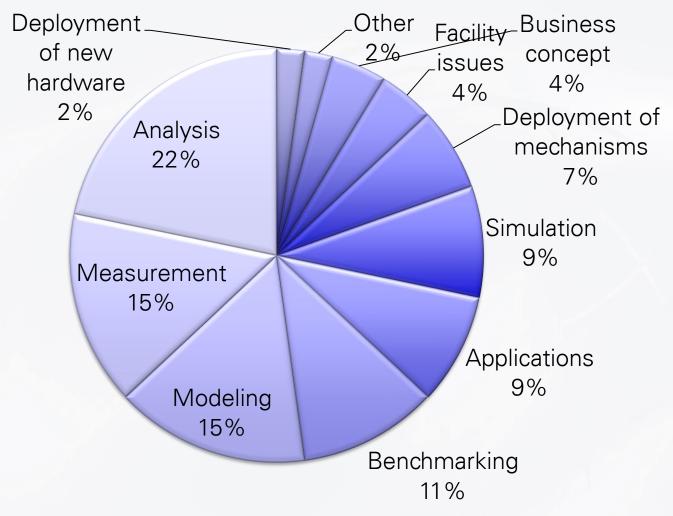
- After 3 years in Hamburg, the first time in Dresden
- 10 scientific talks
 - From device scale to data center scale
 - Modeling and measurement
- 5 vendor talks
 - How can energy efficient HPC systems be implemented?
 - How should we cool systems efficiently?
 - What is the future of DRAM?







Statistics (1) Submission Topics

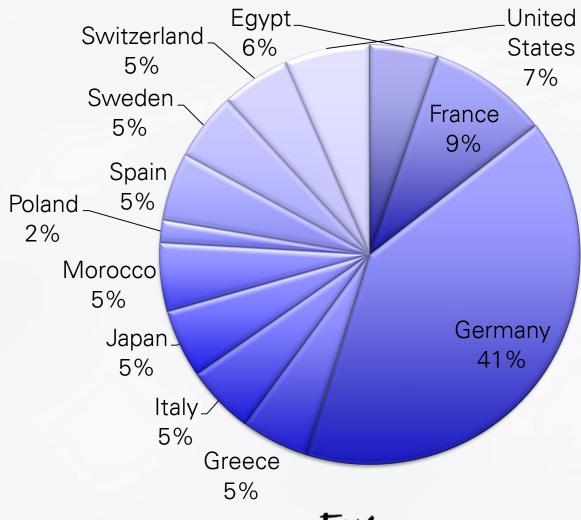








Statistics (2) Submissions Origin









Whom to thank?

Conference chairs

Wolfgang E. Nagel, Thomas Ludwig, Matthias Müller

Program committee

Cosimo Anglano, Costas Bekas, Dzmitry Kliazovich, Enrique S. Quintana-Orti, Francisco Javier García Blas, Jean-Marc Pierson, Julita Corbalan, Kalyan Kumaran, Laurent Lefevre, Natalie Bates, Simon McIntosh-Smith, Vincent Heuveline, William Barton Sawyer

Local organizer

Robert Schöne

Sponsors

Bull, IBM, Intel, Megware, Samsung, Gauss Allianz

DKRZ / University Hamburg staff

esp. Michaele Hensel, Michal Kuhn

ZIH staff







Preview 7th Parallel Tools Workshop

- More and more tools include energy efficiency metrics
- We are not the only ones that provide such metrics, but this is how we include it in our analysis tool-suites

Temperature

Cache Misses

Power consumption

Proc. sleep state

Proc. frequency

Avg. proc. frequency









Last words

- Be sure, that we have your slides
- The slides will be published on the website
- The photos will also be on the website
- If you need anything (call a taxi, get a lunch ticket), please tell us
- Have a nice stay / journey
- The flash drives are biodegradable (polylactic acid)

See you next year





